

Inventor(s): Ol et al.

Appl. No.: 09

Series Code ↑

421,322

Serial No.

Filed: October 18, 1999

Hon. Commissioner of Patents
Washington, D.C. 20231

Group Art Unit

2815

Examiner:

ECKERT II, G

Atty. Dkt.

P

264817

OSP-8303

M#

Client Ref

Appl. Title:

DIELECTRICALLY SEPARATED
WAFER AND METHOD OF
MANUFACTURING THE SAME

Sir:

REPLY/AMENDMENT/LETTER

Date: May 28, 2002

This is a reply/amendment/letter in the above-identified application and includes the herewith attachment of same date and subject which is incorporated hereinto by reference and the signature below is treated as the signature to the attachment in absence of a signature thereto.

FEE REQUIREMENTS FOR CLAIMS AS AMENDED

1. Small Entity claim

- A. ☒ NOT made
B. ☐ Withdrawn
C. ☐ made herewith
D. ☐ made previously

For B & C
See **Required**
Separate Paper
(Pat-256)

Claims remaining after amendment	Highest number previously paid for	Present Extra	Large/Small Entity	Additional Fee	Fee Code Lg/Sm
2. Total Effective Claims	5	**minus 20 0	x \$18/\$9 =	+ \$0	103/203
3. Independent Claims	3	***minus 6 0	x \$84/\$42 =	+ \$0	102/202
4. If amendment enters proper multiple dependent claim(s) into this application for first time (leave blank if this is a reissue application)..... add			+ \$280/\$140 =	+ \$0	104/204
5. Original due Date: June 15, 2002	<input type="checkbox"/> NONE				
6. Petition is hereby made to extend the original due date to cover the date this response is filed for which the requisite fee is attached	(1 mo) (2 mos) (3 mos) (Usable only for ≤ 2mo.OA --- 4 mos) (Usable only for 30 day/1mo.OA --- 5 mos)	\$110/\$55 = \$400/\$200 = \$920/\$460 = \$1,440/\$720= \$1,960/\$980=	+ \$0		115/215 116/216 117/217 118/218 128/228
7. Enter any previous extension fee paid since above original due date and subtract		- \$0			
8.			Extension Fee	+ \$0	
9. If Terminal Disclaimer attached, add Rule 20(d) official fee		+ \$110/\$55	+ \$0		148/248
10. If IDS attached requires Official Fee under Rule 97 (c), add		+ \$180	+ \$180		126 126
or if Rule 97(d) Request add		+ \$180			
11. After-Final Request Fee per rules 129(a) and 17(r)		+ \$740/370	+ \$0		146/246
12. No. of additional inventions for examination per Rule 129(b)		x \$740/370 ea	+ \$0		149/249
13. Request for Continued Examination (RCE)		+ \$740/370	+ \$0		1179/1279
14. Petition fee for			+ \$0		

15. TOTAL FEE = \$180

16. *If the entry in this space is less than entry in next space, the "Present Extra" result is "0".

17. **If the "Highest number previously paid for" in this space is less than 20, write "20" in this space.

18. ***If the "Highest number previously paid for" in this space is less than 3, write "3" in this space.

PLEASE CHARGE
OUR DEP. ACCT.

Our Deposit Account No. 03-3975)

(Our Order No. 61063

264817

C#

M#

CHARGE STATEMENT: The Commissioner is hereby authorized to charge any fee specifically authorized hereafter, or any missing or insufficient fee(s) filed, or asserted to be filed, or which should have been filed herewith or concerning any paper filed hereafter, and which may be required under Rules 16-18 (missing or insufficiencies only) now or hereafter relative to this application and the resulting Official Document under Rule 20, or credit any overpayment, to our Accounting/Order Nos. shown above, for which purpose a duplicate copy of this sheet is attached.

This CHARGE STATEMENT does not authorize charge of the issue fee until/unless an issue fee transmittal sheet is filed.

Query: Is appeal deadline now? If so, file Notice of Appeals separately.

1600 Tysons Boulevard
McLean, VA 22102
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Pillsbury Winthrop LLP
Intellectual Property Group

By Atty: John P. Darling

Sig:

Reg. No.

Fax:

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(703) 905-2900

(703) 905-2045

Atty/Sec: JPD/tis

NOTE: File this cover sheet in duplicate with PTO receipt (PAT-103A) and attachments



REPLY UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
TECHNOLOGY CENTER ART UNIT 2815

#11/B
6-4-02
P. Charles
10/9/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION OF

Confirmation No.: 3971

OI *et al.*

Group Art Unit: 2815

Application No. 09/421,322

Examiner: ECKERT II, G

Filed: October 18, 1999

Title: DIELECTRICALLY SEPARATED WAFER AND METHOD
OF MANUFACTURING THE SAME

May 28, 2002

* * * * *

AMENDMENT AFTER FINAL REJECTION UNDER 37 C.F.R. §1.116

Hon. Commissioner of Patents
Washington, D.C. 20231

Sir:

In response to the Office Action dated March 15, 2002 please amend the above-identified application as follows.

IN THE CLAIMS

Please amend claims 1, 3 and 5 as follows:

RECEIVED
MAY 31 2002
TC 2800 MAIL ROOM

1. (Twice Amended) A dielectrically separated wafer having a plurality of dielectrically separated monocrystalline silicon islands mutually defined by a dielectrically separating oxide film on a surface of the wafer, wherein said dielectrically separated silicon islands comprise:

a high concentration impurity layer formed on a bottom of the islands in a flat plate form; and